Europass
Curriculum Vitae

Personal information

First name(s) / Surname(s)	**Ciprian Vasile, RADU**
Address(es)	35, Sat Daia, RO-557213, Sibiu, Romania
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Telephone(s)	Mobile: +40(0)740055831
E-mail	radu_ciprianro@yahoo.com
Nationality	Romanian
Date of birth	04.09.1984 (dd.mm.yyyy)
Gender	Male

Work experience

Dates	01.10.2011 onwards
Occupation or position held	Teaching Assistant
Main activities and responsibilities	- Laboratory classes: Software Engineering;
	- Linux administration of the "Lucian Blaga" University High Performance Computing (HPC) System (15 Intel Xeon quad cores and 4 IBM Cell B.E. cores).
Name and address of employer	Computer Engineering Department, Faculty of Engineering, "Lucian Blaga" University, Sibiu, Romania
Type of business or sector	Academic
Dates	01.11.2009 – 31.03.2010
Occupation or position held	Research Assistant
Main activities and responsibilities	Develop a Network-on-Chip simulator
Name and address of employer	Systems and Networking Department, chair: Prof. Theo Ungerer, PhD, University of Augsburg, Germany
Type of business or sector	Academic
Dates	21.08.2006 – 02.10.2009
Occupation or position held	Software Engineer
Main activities and responsibilities	- Software development (mainly in Java);
	- Software development life cycle;
	- Software documentation development (user guides);
	- Database implementation (Oracle, MSSQL, Apache Derby).
Name and address of employer	Ropardo SRL, Sibiu, Romania
Type of business or sector	Software Development

Education and training

Dates	01.11.2008 – 30.11.2011
Title of qualification awarded	PhD in Computers and Information Technology
Principal subjects/occupational skills covered	**Optimized Algorithms for Network-on-Chip Application Mapping**, PhD Thesis, "Lucian Blaga" University of Sibiu, Romania, November 25th 2011, Sibiu, Romania. PhD supervisor: Prof. Lucian Vințan Evaluation comity: Prof. Nicolae Țăpus (Polytechnic University of Bucharest), Prof. Valentin Cristea (Polytechnic University of Bucharest) and Prof. Mircea Popa (Polytechnic University of Timișoara) PhD thesis objective: evaluate and optimize single objective and multi-objective heuristic algorithms for Network-on-Chip application mapping. I published 8 papers in international and national conferences. Other 2 are submitted for publication. PhD thesis and published papers are available at http://webspace.ulbsibiu.ro/ciprian.radu/ My PhD thesis received positive appreciation letters from 21 international and national specialists. I briefly mention some of them: Prof. dr. Avi Mendelson – **Microsoft** R&D Center (**Israel**), Prof. dr. Wolfgang Rosenstiel – dean of University of Tübingen (**Germany**), Prof. dr. Sorin Coțofană – University of Delft (**Netherlands**), Prof. dr. ing. Marius Linguraru – Sheikh Zayed Institute (**Washington DC, U.S.**), Prof. dr. Traian Muntean – Université Aix Marseille (**France**), Prof. dr. ing. Liviu Iftode – Rutgers University, (**New Jersey, U.S.**), Assoc. Prof. dr. Cristina Silvano – Politecnico di Milano (**Italy**), Assoc. Prof. dr. Robert Dick – University of Michigan (**U.S.**) and many others from Romania (Technical University of Cluj-Napoca, Polytechnic University of Timișoara, Polytechnic University of Bucharest, Technical Military Academy of Bucharest, Transilvania University of Brașov, Technical University of Iași, „Lucian Blaga" University of Sibiu). I developed UniMap (https://code.google.com/p/unimap/), a unified open source software framework (developed in Java and C++) for the evaluation and optimization of Network-on-Chip application mapping algorithms.
Name and type of organisation providing education and training	Advanced Computer Architecture & Processing Systems (ACAPS – http://acaps.ulbsibiu.ro/) Research Lab, chair: Prof. Lucian Vințan, PhD, "Lucian Blaga" University, Sibiu, Romania
Level in national or international classification	ISCED 6
Dates	28.10.2010 – 29.10.2010
Title of qualification awarded	Diploma
Principal subjects/occupational skills covered	National Training Program in Scientific Authorship
Name and type of organisation providing education and training	Romanian Ministry of Education
Dates	11.07.2010 – 17.07.2010
Title of qualification awarded	Certificate of Attendance
Principal subjects/occupational skills covered	Application-Specific (MP)SoC Architectures for Internet Networking Fast and Accurate Computer System Simulators Fault Tolerant Computer Architecture Embedded Systems: A Software Perspective
Name and type of organisation providing education and training	ACACES 2010, Sixth International Summer School on Advanced Computer Architecture and Compilation for Embedded Systems, HiPEAC consortium, Barcelona, Spain
Dates	12.10.2009 – 14.10.2009
Title of qualification awarded	Certificate of Completion
Principal subjects/occupational skills covered	Parallel Programming (OpenMP, Intel Threading Building Blocks)
Name and type of organisation providing education and training	Intel Parallelism Faculty 3.0, Polytechnic University of Bucharest, Romania
Dates	12.07.2009 – 18.07.2009

Title of qualification awarded	Certificate of Attendance
Principal subjects/occupational skills covered	Power- and Reliability-Aware Microarchitecture Performance, Scalability and Real-Time Response From the Linux Kernel Transactional Memory Optimizations for multicore and GPGPU architectures
Name and type of organisation providing education and training	ACACES 2009, Fifth International Summer School on Advanced Computer Architecture and Compilation for Embedded Systems, HiPEAC consortium, Barcelona, Spain
Dates	06.07.2009 – 10.07.2009
Title of qualification awarded	Certificate of Attendance
Principal subjects/occupational skills covered	Large-Scale Continuous Testing of Highly Configurable Systems Test-Driven Development: Practice and Theory Agile Software Process Management Non-invasive Software Product and Process Measurement
Name and type of organisation providing education and training	CASE Summer School on Applied Software Engineering, Free University of Bozen – Bolzano, Italy
Dates	01.10.2003 – 20.07.2008
Title of qualification awarded	Bachelor Degree in Computer Science (Diplomat Engineer)
Principal subjects/occupational skills covered	BSc Thesis: **Implementing a Multicore Shared Memory Architecture using Transaction Level Modelling with UNISIM**, Supervisors: Prof. Lucian Vinţan, PhD; Assoc. Prof. Adrian Florea, PhD BSc Thesis – short description: Using UNISIM and Transaction Level Modelling, I have extended the full system TLM simulator provided by UNISIM to a dual core Shared Memory Architecture. I have addressed some common multicore issues like cache coherence (MSI, MESI and MOESI protocols), sequential consistency, synchronization and communication, using some of the SPLASH-2 and PARSEC benchmarks. I graduated as **head of class**. BSc Thesis grade 10.00 (highest grade is 10.00) Overall average of the years of study 9.93 (highest grade is 10.00) Main topics covered: Informatics, Mathematics, Hardware Architectures, Data Structures and Algorithms, Software Architectures Design and Development, Design Patterns, Operating Systems, Electrical Basis, Electric Circuits, Digital Signal Processing, Formal Languages and Compilers, Data Compression and Encryption, Computer Vision, Data Mining, Agent Oriented Programming As student, I designed ABPS, an Advanced Branch Prediction Simulator, with which I obtained (in 2007) first place at the Students Communication Session and second place at the Students Projects Contest (both competitions were organized at the faculty I attended). I published ABPS at an International Conference held in Slovenia.
Name and type of organisation providing education and training	Faculty of Engineering, "Lucian Blaga" University, Sibiu, Romania
Level in national or international classification	ISCED 5
Dates	24.02.2006 – 18.08.2006
Principal subjects/occupational skills covered	Software Development Training I worked with Java and Java related technologies and I developed a small search engine with which I participated in a students' projects contest.
Name and type of organisation providing education and training	Ropardo SRL, Sibiu, Romania
Dates	15.09.1999 – 20.07.2003
Title of qualification awarded	Computer Operator
Principal subjects/occupational skills covered	Mathematics and Information Technology
Name and type of organisation providing education and training	"Gheorghe Lazăr" High School, Sibiu, Romania

| Level in national or international classification | ISCED 4 |

Personal skills and competences

| Mother tongue(s) | **Romanian** |

Other language(s)

Self-assessment	Understanding				Speaking				Writing	
European level (*)	Listening		Reading		Spoken interaction		Spoken production			
English	B2	Independent user	B2	Independent user	B2	Independent user	B2	Independent user	B2	Independent user
French	A2	Basic user	A2	Basic user	A2	Basic user	A2	Basic user	A2	Basic user

(*) Common European Framework of Reference for Languages

Social skills and competences

- Team work: I have worked in various types of teams like software developer teams and research teams. I worked as a software developer in Sibiu and as a researcher in Sibiu and in Augsburg, Germany;
- Good communication skills: During my PhD research, I have had the opportunity to discuss with Professors and researchers at the highest international level from Computer Science;
- Intercultural skills: Besides my abroad work in Germany, I have been at international summer schools and courses with PhD students from the entire world.

Organisational skills and competences

As software engineer, I coordinated a small team of programmers, during the final phase of a software project. As PhD student, I coordinated one student and two master students for their bachelor and respectively dissertation theses.

Other skills and competences

Conscientious, with analytic thinking

Driving licence

Category B

Additional information

References supplied on request

Annexes

Projects involvement as Software Engineer

Project description

Ropardo:jPPM – Java Portfolio and Project Management
- Web start based application for systematic portfolio and project management. This software product supports the planning, prioritization, integrated controlling and control of all of your enterprise projects. You can supervise and steer all important project areas as achievement, dates, costs, resources and risks;
- Can be deployed on any database infrastructure (Oracle, MSSQL).

Technologies

Swing, SimpleORM, Jasper Reports, DynamicJasper, iText, jFreeChart, DJT Gantts, JaxMe 2, JAXB, XStream, Oracle, MSSQL, UIFramework

Project description

Ropardo:IS – Information System
- Data mining tool for transforming enterprise data into defined and comprehensible information. This means data becomes characteristic numbers with substantial factors of influence for the entire flow of an enterprise;
- Uses a client-server approach.

Technologies

Swing, XML-RPC, SimpleORM, application server (tomcat), webservices, Oracle, MSSQL, Jasper Reports, iText, UIFramework

Project description

Ropardo:IS EDTL System
- C program that allows scheduling data imports in the Data Warehouse (DWH) of Ropardo:IS. While data is imported, several processing steps can be applied, thus allowing managing (transforming) data which already exists in the DWH;
- The whole process can run automatically; errors and warnings which might occur during processing are registered in the database.

Technologies

Oracle Pro*C, Oracle SQL*Loader

Project description	**Ropardo:IS Meta**
	Metadata management tool for the Ropardo:IS system, meant to ease and allow configuration of the data mining process.
Technologies	Swing, SimpleORM, Oracle, UIFramework
Project description	**Experience DB**
	- Research project, part of KoBaS project;
	- Experience DB is a database of experiences, capable of retrieving the situations (encountered in the past) that are most similar with a new given situation;
	- Uses a Case-Based Reasoning (CBR) engine, suitable for Knowledge Management (KM) issues;
	- The knowledge can also be extracted from Ontology (OWL) files.
Technologies	Swing, XML, web services, MySQL, jColibri, protégé, Jena